1A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

FEATURES:

- · Glass Passivated Chip Junction
- Reverse Voltage 1400 V
- Forward Current 1A
- · High Surge Current Capability
- Designed for Surface Mount Application

MECHANICAL DATA

· Case: ABS/LBF

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 88mg / 0.0031oz

• Chip size:50mil

Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

4 2 2
ABS/LBF Package

DESCRIPTION

Input Pin (~)

Input Pin (~)

Output Anode (+)

Output Cathode (-)

PINNING

PIN

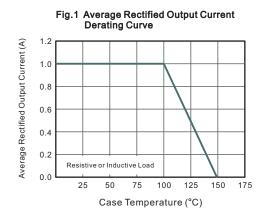
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Parameter	Symbols	ABS14-10	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	1400	V
Maximum RMS voltage	V _{RMS}	980	V
Maximum DC Blocking Voltage	V _{DC}	1400	V
Average Rectified Output Current at T _c = 125 °C	Io	1	А
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	I _{FSM}	30	А
Forward Voltage per element @I _F =1A	V _F	1.1	V
Maximum DC Reverse Current @T _A =25 °C at Rated DC Blocking Voltage @T _A =125 °C	I _R	3 100	μA
Typical Junction Capacitance (Note1)	C _j	13	pF
Typical Thermal Resistance (Note2)	$R_{ heta JA} \ R_{ heta JC}$	72 20	°C/W
Operating and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4 V D.C.

2. Mounted on glass epoxy PC board with 4×1.5"×1.5" ($3.81\times3.81\,\text{cm}$) copper pad.





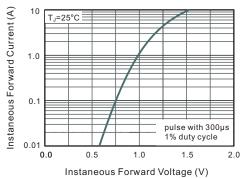


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

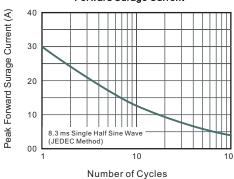


Fig.2 Typical Reverse Characteristics

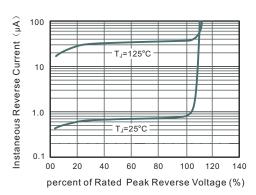
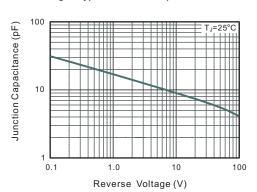


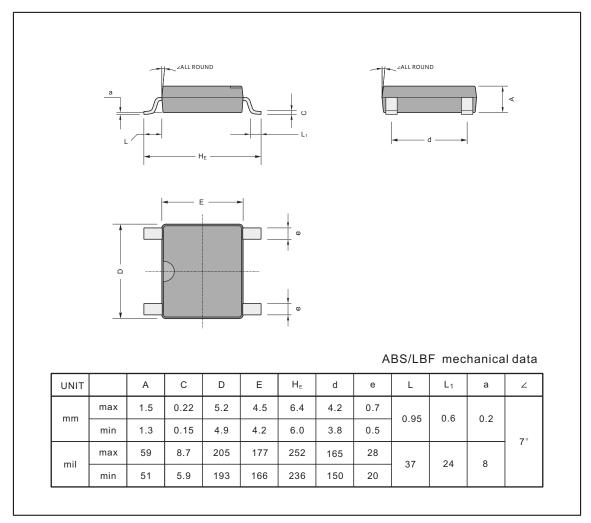
Fig.4 Typical Junction Capacitance



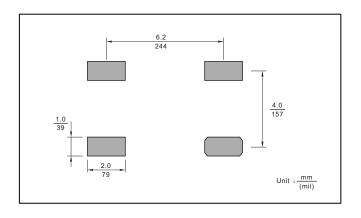
PACKAGE OUTLINE

Plastic surface mounted package; 4 leads

ABS/LBF



The recommended mounting pad size



Marking

Type number	Marking code
ABS14-10	10T14



文件履历表

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序号	制/修订日期	生效日期	版次	修订内容	变更原因	制/修订人	备注
01	2022. 8. 30	2022.8.	Rev 1.1	初版制定	/	张雷	
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